

FIG. 1

Defective pixel address
(row addr + col. addr)

index → 0	0000_0000_0000_0000_1000	10
1	0000_0000_0000_0000_1001	
2	0000_0000_0000_0000_1010	
3	0000_0000_0000_0000_1011	
4	0000_0000_0000_0010_0000	
5	0000_0000_0000_0010_0001	10' Window range
6	0000_0000_0000_0010_0010	
7	0000_0000_0000_0010_0011	
	⋮	
125	0000_1000_0000_0000_0000	
126	0000_1000_1000_0000_0000	
127	0000_1000_1100_0000_0000	

FIG. 2

Defective pixel address (Row addr + Col. addr)	
index → 0	0000_0000_0000_0000_0100
1	0000_0000_0000_0000_0101
2	0000_0000_0000_0000_0110
	⋮
126	1111_1111_1111_1111_1111
127	1111_1111_1111_1111_1111

FIG. 3

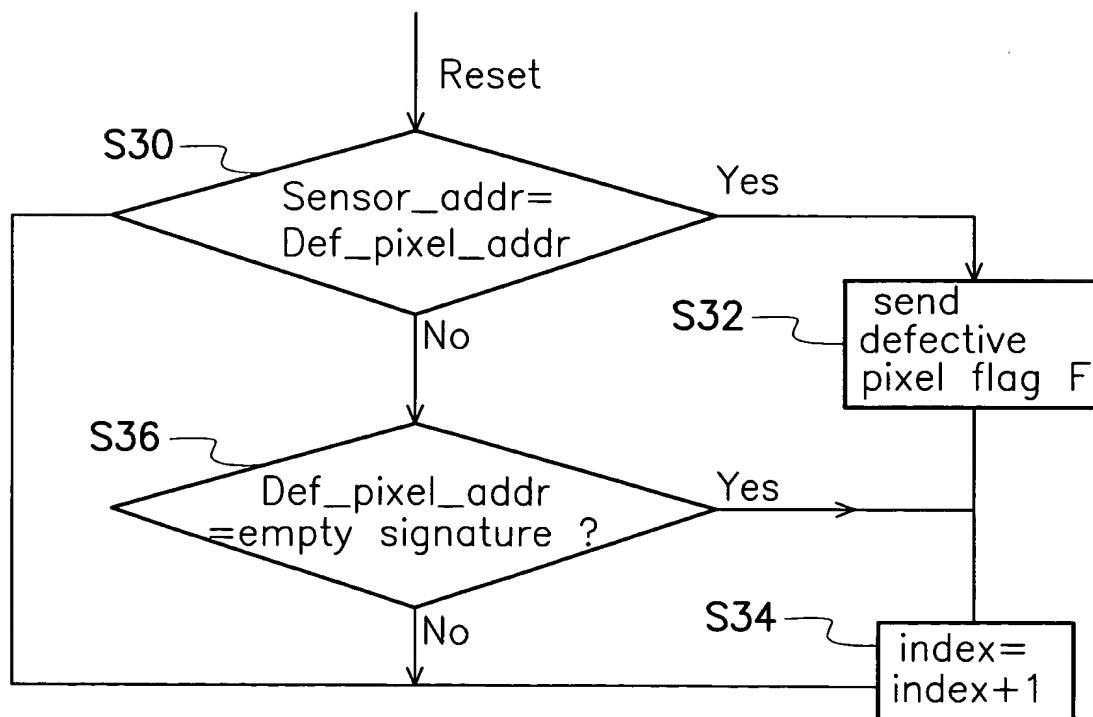


FIG. 4